Electronic Patent Application Fee Transmittal							
Application Number:	10	10716309					
Filing Date:	17-Nov-2003						
Title of Invention:	Te	Techniques for grouping circuit elements into logic blocks Ketan Padalia					
First Named Inventor/Applicant Name:	Ketan Padalia						
Filer:	David Brian Raczkowski/Lisa Jeanetta						
Attorney Docket Number:	015114-069200US						
Filed as Large Entity							
Utility Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Extension-of-Time:							
Extension - 2 months with \$0 paid		1252	1	450	450		

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)				
Miscellaneous:								
Request for continued examination	1801	1	790	790				
	Tota	1240						